

ABSTRACT

A method and apparatus is disclosed for improving high frequency
5 performance of an amplifier, such as for example, a current mirror. In one
embodiment, a delay element is introduced in a current mirror signal path to account
for signal propagation delay that may exist in one or more alternative signal paths.
The delay element maintains desired phase alignment at a cascode node of the current
mirror thereby establishing, in one embodiment, the cascode node (V_c) in an AC
10 ground state. To extend current mirror high frequency capability an embodiment is
disclosed having cross-coupled capacitors, active elements, or one or more other
devices configured to provide positive feedback to one or more current mirror inputs.
The positive feedback may be selectively configured to increase the operational
bandwidth of the current mirror.

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